#### © 2024 Advanced Micro Devices, Inc. All rights reserved.

The information contained herein is for informational purposes only, and is subject to change without notice. While every precaution has been taken in the preparation of this document, it may contain technical inaccuracies, omissions and typographical errors, and AMD is under no obligation to update or otherwise correct this information. Advanced Micro Devices, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this document, and assumes no liability of any kind, including the implied warranties of noninfringement, merchantability or fitness for particular purposes, with respect to the operation or use of AMD hardware, software or other products described herein. No license, including implied or arising by estoppel, to any intellectual property rights is granted by this document. Terms and limitations applicable to the purchase or use of AMD's products are as set forth in a signed agreement between the parties or in AMD's Standard Terms and Conditions of Sale. Any unauthorized copying, alteration, distribution, transmission, performance, display or other use of this material is prohibited.

#### **Trademarks**

AMD, the AMD Arrow logo, AMD AllDay, AMD Virtualization, AMD-V, PowerPlay, Vari-Bright, and combinations thereof are trademarks of Advanced Micro Devices, Inc. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies.

Dolby is a trademark of Dolby Laboratories.

HDMI is a trademark of HDMI Licensing, LLC.

HyperTransport is a licensed trademark of the HyperTransport Technology Consortium.

Microsoft, Windows, Windows Vista, and DirectX are registered trademarks of Microsoft Corporation in the US and/or other countries.

MMX is a trademark of Intel Corporation.

OpenCL is a trademark of Apple Inc. used by permission by Khronos.

PCIe is a registered trademark of PCI-Special Interest Group (PCI-SIG).

USB Type-C<sup>®</sup> and USB-C<sup>®</sup> are registered trademarks of USB Implementers Forum.

Reverse engineering or disassembly is prohibited.

USE OF THIS PRODUCT IN ANY MANNER THAT COMPLIES WITH THE MPEG ACTUAL OR DE FACTO VIDEO AND/OR AUDIO STANDARDS IS EXPRESSLY PROHIBITED WITHOUT ALL NECESSARY LICENSES UNDER APPLICABLE PATENTS. SUCH LICENSES MAY BE ACQUIRED FROM VARIOUS THIRD PARTIES INCLUDING, BUT NOT LIMITED TO, IN THE MPEG PATENT PORTFOLIO, WHICH LICENSE IS AVAILABLE FROM MPEG LA, L.L.C., 6312 S. FIDDLERS GREEN CIRCLE, SUITE 400E, GREENWOOD VILLAGE, COLORADO 80111.



### **Contents**

Contents	5	3	
Chapter	1	Overview	4
Chapter	2	Linux® Kernel Support	4
Chapter	3	Linux Distribution Support	4
Chapter	4	Component Versions	5
Chapter	5	Features Supported on Ryzen <sup>TM</sup> Embedded processors	6
Chapter	6	Features Supported on Ryzen <sup>TM</sup> Embedded processors with iGPU	9
6.1	Disp	olay Support:	9
6.2	HW	Codec Support	10
Chapter	7	Platforms Supported	11
Chapter	8	Tested Platform Configurations	12
Chapter	9	Issues Fixed	14
Chapter	10	Known Issues/Limitations	14
Chapter	11	Support	17

### **Chapter 1** Overview

**Note:** This Ubuntu software package shall be used for evaluation purposes only. Customers using this package in production environments or using this package for further distribution must ensure that Ubuntu license terms are adhered to. Contact your AMD FAE for more information.

AMD's Linux® Driver includes an open source graphics driver for AMD's embedded platforms and other peripheral devices on selected development platforms.

New features supported in this release:

- Kernel Migration to 6.6.30 LTS.
  - eSPI Rel-4 driver patch porting on new 6.6.30 kernel
  - SPI0 NAND support patch porting on new 6.6.30 kernel
- Bug Fixes

### **Chapter 2 Linux<sup>®</sup> Kernel Support**

• 6.6.30 LTS

### **Chapter 3 Linux Distribution Support**

• Ubuntu 24.04

### **Chapter 4** Component Versions

The following table shows git commit details of the sources and binaries used in the package.

The patches present in the patches folder of this release package must be applied on top of the git commit mentioned in the following table to get the full sources corresponding to this driver release. The sources directory in this package contains patches pre-applied to these commit IDs.

Component Name	Version	Commit ID	Source Link for git clone
Kernel	6.6.30 LTS	5697d159afef8c475f13a0b7b85f09bd4578106c	https://github.com/gregkh/linux/tree/v6.6.30
Libdrm	2.4.120	75254bf2390c10644ffb35a90fc8f18f196f9f0c	https://gitlab.freedesktop.org/mesa/dr m/-/tree/libdrm-2.4.120
Mesa	24.0.7	cc175010c5d9c60b02c2b22d60564e8fb2fc0a55	https://gitlab.freedesktop.org/mesa/me sa/-/tree/mesa-24.0.7
Ddx	23.0.0	7025aefcdf9673665588cf291c5d71beb39cce89	https://gitlab.freedesktop.org/xorg/ driver/xf86-video-amdgpu/- /tree/xf86-video-amdgpu-23.0.0
Wayland	1.22.0	b2649cb3ee6bd70828a17e50beb16591e6066288	https://gitlab.freedesktop.org/wayland/ wayland
libva	2.21.0	0b01aed44ef1a6ad660261284ff266fa812829ef	https://github.com/intel/libva.git
LLVM	18.1.5	617a15a9eac96088ae5e9134248d8236e34b91b1	https://github.com/llvm/llvm-project
Firmware	Main	b9d2bf23459e7dea7811b867ad189359ad0e4409	https://git.kernel.org/pub/scm/linux/ke rnel/git/firmware/linux-firmware.git
Vulkan	2024.Q2.1	80ca649026dcce23974de0832c3277bb715f27d4	https://github.com/GPUOpen- Drivers/AMDVLK/tree/v-2024.Q2.1
		Supported Applications	
LunarG Vulkan SDK	1.3.283.0	NA	https://vulkan.lunarg.com/sdk/home#linux
Vulkan CTS	1.3.8.3		https://github.com/KhronosGroup/ VK-GL-CTS/tree/vulkan-cts-1.3.8.3
RGP	2.1	NA	https://github.com/GPUOpen- Tools/radeon_gpu_profiler/tree/v2.1

### Chapter 5 Features Supported on Ryzen<sup>TM</sup> Embedded processors

Supported features are shown in the following table.

Feature Group	Feature Supported	V1500	V2000	R2000	V3000
2D	2D acceleration	NA	Yes	Yes	NA
	EGL 1.4, 1.5, EGL extensions.	NA	Yes	Yes	NA
	OGL 4.5,	NA	Yes	Yes	NA
20	OGL 4.6				
3D	GLX 1.4	NA	Yes	Yes	NA
	DRI3 support	NA	Yes	Yes	NA
	DRI3 updates (VDPAU, VAAPI)	NA	Yes	Yes	NA
	Vulkan Open Source	NA	Yes	Yes	NA
2D	10 bit Display	NA	No	No	NA
Audio	DP Audio supports for standard	NA	Yes	Yes	NA
Audio	I2S Audio	NA	Yes	Yes	NA
Display	EDID (Basic)	NA	Yes	Yes	NA
	X and Desktop support	NA	Yes	Yes	NA
	Tear Free Desktop	NA	Yes	Yes	NA
	Partial support RandR 1.4 capabilities	NA	Yes	Yes	NA
	Kernel Mode Setting	NA	Yes	Yes	NA
	4K60Hz display support	NA	Yes	Yes	NA
Display support	Multi-GPU support (see table below for dGPU pairing)	NA	No	No	NA
	Number of displays supported (see display support table below)	NA	-	-	NA
	4K cinema	NA	Yes	No	NA
	DP MST	NA	Yes	Yes	NA
	Single Large Surface (SLS)	NA	Yes	No	NA
	Play back support MPV player using VAAPI / VDPAU	NA	Yes	Yes	NA
	Play back support for Gstreamer using VAAPI, gstomx (not recommended)	NA	Yes	Yes	NA
Play back	1080p 24fps, 30 fps and 60fps video play back	NA	Yes	Yes	NA
	4k 30fps video play back	NA	Yes	Yes	NA
	4k 60fps video play back	NA	Yes	Yes	NA
Power Management Power Play support to re-clock		NA	Yes	Yes	NA

Feature Group	Feature Supported	V1500	V2000	R2000	V3000
	Initial GPU reset support	NA	Yes	Yes	NA
	Power Play sysfs interface for manually selecting clock speeds	NA	NA	Yes	NA
	S3	Yes	Yes	Yes	Yes
	S5	Yes	Yes	Yes	Yes
VDPAU Post Processing	Deinterlace	NA	Yes	Yes	NA
VDPAU Post Processing	Edge Enhancement	NA	Yes	Yes	NA
VAAPI Postprocessing	Deinterlace	NA	Yes	Yes	NA
Transcode	4k Encode	NA	Yes	No	NA
	Scaling and color space conversion (CSC)	NA	Yes	Yes	NA
Video Quality	Pull down detection and Deinterlacing	NA	Yes	Yes	NA
· ·	Support for software scaling	NA	Yes	Yes	NA
	Support for hardware scaling	NA	No	Yes	NA
	10-bit Decode with 10-bit render	NA	NA	Yes	NA
Compute	OpenCL	NA	No	No	NA
dGMA -OpenGL		NA	Yes	NA	NA
dGMA - OpenCL		NA	No	NA	NA
fTPM		Yes*	Yes	Yes	Yes
	10M	Yes*	NA	NA	Yes
	100M	Yes*	NA	NA	Yes
RJ45-10G-Base-T	1G	Yes*	NA	NA	Yes
(Marvell PHY)	2.5G	No	NA	NA	Yes
	10G	Yes*	NA	NA	Yes
	10M	No	NA	NA	Yes
	100M	No	NA	NA	Yes
CEP (	1G	Yes*	NA	NA	Yes
SFP+ (connector)	10GBASE_KR [AN=OFF, ON]	NA	NA	NA	Yes
	2.5G [AN=OFF]	NA	NA	NA	Yes
	10G	Yes*	NA	NA	Yes
AIC1 – Inphi CS4223 Optical Fiber PHY (SFP+)	10M/100M/1G/10G	NA	NA	NA	Yes
AIC1 – TI DS125 Series Re-Timer (SFP+)	10M/100M/1G/10G	NA	NA	NA	Yes
AIC2 – 1G-Base-T (Marvell 88E1512P)	10M/100M /1G	NA	NA	NA	Yes
AIC2 – 10G-Base-T (Marvell AQR113C)	10M/100M/1G/2.5G/10G	NA	NA	NA	Yes
AIC3 – 1G-Base-T	10M/100M /1G	NA	NA	NA	Yes

Feature Group Feature Supported		V1500	V2000	R2000	V3000
(Broadcom BCM54220)					
AIC3 – 10G-Base-T (Broadcom BCM84892)	100M/1G/2.5G/10G	NA	NA	NA	Yes
	BC	Yes	Yes	Yes	No
-MMC	HS200	Yes	Yes	Yes	No
eMMC	HS400	Yes	Yes	Yes	No
	(USB/PCIe to eMMC bridge)	No	No	No	Yes
	SD UHS I – SDR50	Yes	Yes	Yes	No
SD Card	SD UHS I – SDR104	Yes	Yes	Yes	No
	SD UHS I – SDR104	Yes	Yes	Yes	No
	I2C	Yes	Yes	Yes	Yes
	USB	Yes	Yes	Yes	Yes
	USB 4.0	No	No	No	Yes
	SATA	Yes	Yes	Yes	Yes
Peripherals (I/O)	UART	Yes	Yes	Yes	Yes
	WDT	Yes	Yes	Yes	Yes
	SMBUS	Yes	Yes	Yes	Yes
	SPI Kernel Driver	Yes**	Yes**	Yes**	Yes**
	eSPI	No	No	No	Yes

<sup>\*</sup>Bilby platform only

<sup>\*\*</sup>To use the SPI kernel driver on Bilby/Fox platforms, the BIOS which has enabled SPI Entry in the ACPI table is required. The default BIOS does not have this feature. Please contact your FAE for the required BIOS.

## Chapter 6 Features Supported on Ryzen<sup>TM</sup> Embedded processors with iGPU

The 2024.20 Linux driver is **not supported** by any AMD Embedded dGPUs.

HW codec and display support is only applicable to Ryzen Embedded processors with integrated graphics.

### **6.1** Display Support:

Platform	Max Number of external 4K display(s)		
	R2312		
R2000	R2314	3	
K2000	R2514		
	R2544	4	
V2000	4		
V1500	NA		
V3000	NA		

### 6.2 HW Codec Support

Codec	API	Middleware/Framework
H.264 decode	VAAPI	ffmpeg-VAAPI, gst-VAAPI
H.265 decode	VAAPI	ffmpeg-VAAPI, gst-VAAPI
MPEG2 decode	VAAPI	ffmpeg- VAAPI, gst-VAAPI
VC1 decode	VAAPI	ffmpeg- VAAPI, gst- VAAPI
H.264 encode	VAAPI	gst-VAAPI
VP9 decode	VAAPI	Ffmpeg-VAAPI

### **Chapter 7** Platforms Supported

Embedded SoC Version	Models/OPN's	AMD Customer Reference board
Ryzen Embedded V3000 Series	V3C48, V3C44, V3C18I, V3C18, V3C16, V3C14, V3G18i, V3G48	FOX
Ryzen Embedded <b>V2000</b> Series with AMD Radeon Graphics	V2748, V2546, V2718, V2516	CELADON
Ryzen Embedded V1500	YE1500C4T4MFH, YE1500C4T4MFB	BILBY
Ryzen Embedded <b>R2000</b> Series with AMD Radeon Graphics	R2314, R2312, R2514, R2544	BILBY

### **Chapter 8** Tested Platform Configurations

The following tables show the system configuration that was used for testing the driver package.

V3000 Series			
CPU	V3000		
OPNs	Latest Revision:     AIC1; AIC2; AIC3; B1-DVT Sampels:     V3C48;V3C18i;V3C44; V3C14; V3C16;V3C18,     V3G18i, V3G48;		
Board Type	Fox, Direct Mount		
TDP	V3C48 (8-core 45W CPU) V3C44 (4-core 45W CPU) V3C18I (8-core 15W CPU extended temperature) V3C18 (8-core 15W CPU) V3C16 (6-core 15W CPU) V3C14 (4-core 15W CPU) V3G18I (8-core 15W CPU) V3G48 (8-core 45W CPU)		
BIOS version	RFX100AA (Insyde) RFE1006B (EDKII)		
Memory (DDR5)	2x16 GB [Direct Mount]		
DIMMs	DDR5, 4800 MT/s		
Storage disk	Samsung M.2 NVME 500 Gb and SATA SSD Crucial 250 Gb		
Ethernet connectors	<ul> <li>10G SFI Optical: Finisar (FTLX8574D3BCV and FTLX8574D3BCL), Intel (FTLX8574D3BCV-IT)</li> <li>10G Optical DAC: Fiberstore (SFPP-A020)</li> <li>10G KR (Backplane): Molex DAC cable (747521101) and AMPHENOL SFP DAC CABLE (571540002)</li> <li>1G Bel SFP [SFP-1GBT-06] and Finisar [FCLF8520P2BTL]</li> </ul>		

V2000		
APU	V2000	
APU TDP	10-25W, 35-54W	
BIOS version	RCO1009A	
VRAM setting	4GB	
RAM	16GB	
Display Convertors / Dongles Used	DP to HDMI, HDMI	
Storage disk	SSD, M.2	

V1500			
CPU	V1500		
OPNs	YE1500C4T4MFH, YE1500C4T4MFB		
TDP	16-25W		
BIOS version	RBB120CA_RV_PCO		
RAM	16GB (2x8GB DDR4 2400)		
Storage disk	M.2 SATA		

R2000 Series			
APU	R2000		
APU TDP	12-25W (R2312), 12-35W (R2314, R2514), 35-54W (R2514)		
BIOS version	RBP1003B		
VRAM setting	4GB		
RAM	16GB		
Display Convertors / Dongles Used	DP to HDMI, HDMI		
Storage disk	SSD, M.2		

### **Chapter 9** Issues Fixed

#### V3000 Issues:

1. Fixed: ESPI Get Virtual wire and Put Virtual wire are not working after S3 cycle.

### **Chapter 10** Known Issues/Limitations

#### **Generic Issues**

1. SW installation time increased due to transition from Debian(install.sh) installation to source build(rbh.sh) installation.

Steps	Time Taken			
	V1500P	V3000	R2000	V2000
sudo ./rbh.shprep kernel_source 2>&1   tee			3m 42s	1m 46s
rbh_prepkernel.log	3m 40s	2m 44s		
sudo ./rbh.shbuild kernel_source 2>&1   tee			1hr 16m	59m 9s
rbh_buildkernel.log	1hr 51m 44s	58m 15s	23s	
sudo ./rbh.shbuildall 2>&1   tee rbh_buildall.log			1hr 11m	58m 11s
	1hr 35m 26s	50m 42s	14s	
sudo ./rbh.shpostinstall 2>&1   tee			7s	7s
rbh_postinstall.log	9s	7s		
Total Time taken			2hr 31m	1hr 59m
	3hr 30m 59s	1hr 51m 48s	27s	13s

#### V3000 Issues:

- 1. Use Ethernet DAC cable of length <= 5 meters.
- 2. V3000 is cpu variant, so make sure to add "nomodeset" in grub param
- 3. V3000 need to use in headless mode. If display is needed, then use E9175 dGPU
- 4. UART provisioned for 1 x4 wire and 4 x2 wire modes only
- 5. On Fox RJ45 and 2.5G or 1G speed selection in BIOS, hot-plug and hot-insert of cable always triggering speed switching to 10G. Issue specific to V3C18i OPN.
- 6. Ethernet stability issues
  - a. AIC1 InPhi Phy 10G link stability issue in P2P mode only; mitigating with switch as link partner instead of another Fox
  - b. Link detection issues on SFP Port 0/1 for 1G speed with FS copper module (SFP-GB-GE-T 1000BASE-T) with Cat 5 UTP cable
  - c. Link up failure issue after S3 on SFP+ Connector, with 1G/100M/10M as speed and 1G Bel modules connected on both the ports

- d. Link up issues after S3 on AIC2-1G-BaseT phy ports
- e. AIC3 BCM 10G phy has link stability issues for 100M/2.5G/1G speed modes, when using "ifconfig <i/f> down" command
  - i. For any BCM related phy issues, get in touch with BCM support
- f. AIC3 Link up success but fails to get DHCP IP for 100M speed with 10G PHY
- 7. V3000: Data rate speeds(upto 1.9 MB/s) are observed at 33Mhz and 66 Mhz with SPI NAND.
- 8. Refer "FOX Platform User Guide (ID: 57102)" from <a href="https://devhub.amd.com/reference-platform/fox/">https://devhub.amd.com/reference-platform/fox/</a> for USB-C J60 port, RJ45, AIC1, AIC2 and AIC3 rework details

#### R2000 Issues:

- 1. Stutter observed for video playback through chromium browser.
- 2. Glitches observed with zoom calls on 3x4k/1080p display configuration.
- 3. Lag observed with Microsoft Teams® video conference, PPT, browser on 3x4k display configuration.
- 4. Stutter and lag observed with 4k videoplay back with thin client config.
- 5. Stutter observed at times on secondary displays with 3x4k config.
- 6. One of the displays goes blank after boot with 3xTrue 2K MST configuration.
- 7. Sporadically Type C to Type C Hot plug fails.
- 8. Minor stutter observed at times with vaapi playback with MST-4K@30hz.

#### **Common Issues:**

- 1. Few of the display blank out with MST hub in 5.4 kernel.
- 2. Failures are observed while running Vulkan CTS-1.3.2.0.
- 3. HP Z27s monitor resolution change does not take effect sometimes. *Recommendation*: Not to use the monitor since the monitor issues HPD pulse during Changing resolution causing to revert to previous/native resolution sometimes.
- 4. Hotplug root node of DP MST monitors in daisy chain or via Hub fails. *Workaround*: To always connect or disconnect monitors in MST configuration one by one and not at root node level.
- 5. All MST displays goes blank while booting with MST Hub and MST off on the monitors.
- 6. Tearing/Stutter observed during 4k@60fps playback on 2x4k monitors. *Workaround*: Use zaphord Head configuration to play 4k@60fps video on multi monitor setup.
- 7. Hard hang observed for Piglit tests. *Workaround*: Piglit test passed without arb\_tessellation\_shader-tes-gs-max-output test cases.
- 8. Issues with refresh rate change/rotate using xrandr command.
- 9. Export MESA\_GLES\_VERSION\_OVERRIDE=3.2 to run OGL ES 3.2 CTS.
- 10. Hot plug results in blank display of one of the monitors when using startx mode.
- 11. B-frame support is not available in vaapi encode.
- 12. Observed Test failures while running OGL CTS(v 4.6)

#### **XGBE:**

- 1. [XGBE]: Force mode(Auto negotiation disabled) is not supported in RJ45.
- 2. Can't concurrently enable SFP+ and RJ45 interfaces.

3. No receive Split header support.

Below is the type of SFP/RJ45 modules used in the XGBE validation of this release.

Туре	Model	Part Number	
1G SFP - RJ45	BEL	SFP-1GBT-06	
1G SFP - RJ45	Finisar	FCLF8521P2BTL	
10G SFP+ passive direct cable	Finisar	F17CC004893	
10G SFP optical	Finisar	FTLX8574D3BCL	
10G SFP optical	Finisar	FTLX851D3BCL	
10G SFP optical	Intel	E10G42BTDABLK	
10G SFP optical	Intel	AFBR-709DMZ-IN2	

#### **Third Party Issues/Limitations:**

- 1. Terminal switching results in hard hang randomly. Issue root caused gnome which is third party component.
  - https://bugs.launchpad.net/ubuntu/+source/gdm3/+bug/1758512.
- 2. Switching to console mode upon hotplug results in soft hang. Issue root caused gnome which is third party component.
  - https://bugs.launchpad.net/ubuntu/+source/gdm3/+bug/1758512.
- 3. Stutter can be observed when stream framerate and monitor refresh rate are different. This is expected phenomenon. Stutter cab be minimized with interpolation option in mpv. But it can introduce corruption and other side effects.

### **Chapter 11** Support

Please contact your Field Applications Engineer for support on this release.