

# **AMD Embedded Linux Driver 2024.30 Release Notes**

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# Chapter 1 Overview

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*Note: This Ubuntu software package shall be used for evaluation purposes only. Customers using this package in production environments or using this package for further distribution must ensure that Ubuntu license terms are adhered to. Contact your AMD FAE for more information.*

AMD's Linux® Driver includes an open source graphics driver for AMD's embedded platforms and other peripheral devices on selected development platforms.

New features supported in this release:

- Kernel Migration to 6.6.43 LTS.
  - eSPI Rel-4 driver patch porting on new 6.6.43 kernel
  - PCI driver for SPI2 controller and SPI NAND support patches porting on new 6.6.43 kernel
- Bug Fixes

# Chapter 2 Linux® Kernel Support

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- 6.6.43 LTS

# Chapter 3 Linux Distribution Support

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- Ubuntu 24.04

## Chapter 4 Component Versions

The following table shows git commit details of the sources and binaries used in the package.

The patches present in the patches folder of this release package must be applied on top of the git commit mentioned in the following table to get the full sources corresponding to this driver release. The sources directory in this package contains patches pre-applied to these commit IDs.

Component Name	Version	Commit ID	Source Link for git clone
Kernel	6.6.43 LTS	58b0425ff5df680d0b67f64ae1f3f1ebdf1c4de9	<a href="https://github.com/gregkh/linux/tree/v6.6.43">https://github.com/gregkh/linux/tree/v6.6.43</a>
Libdrm	2.4.122	ad78bb591d02162d3b90890aa4d0a238b2a37cde	<a href="https://gitlab.freedesktop.org/ mesa/drm/-/tree/libdrm-2.4.122">https://gitlab.freedesktop.org/ mesa/drm/-/tree/libdrm-2.4.122</a>
Mesa	24.2.0	22fafc9824f9afe594e5e3c3d488ef7f47a70a10	<a href="https://gitlab.freedesktop.org/ mesa/ mesa/-/tree/mesa-24.2.0">https://gitlab.freedesktop.org/ mesa/ mesa/-/tree/mesa-24.2.0</a>
Ddx	23.0.0	7025aefcdf9673665588cf291c5d71beb39cce89	<a href="https://gitlab.freedesktop.org/ xorg/driver/xf86-video-amdgpu/-/tree/xf86-video-amdgpu-23.0.0">https://gitlab.freedesktop.org/ xorg/driver/xf86-video-amdgpu/-/tree/xf86-video-amdgpu-23.0.0</a>
Wayland	1.23.0	a156431ea66fe67d69c9fbba8a8ad34dabbab81c	<a href="https://gitlab.freedesktop.org/ wayland/ wayland">https://gitlab.freedesktop.org/ wayland/ wayland</a>
Libva	2.22.0	217da1c28336d6a7e9c0c4cb8f1c303968a675f1	<a href="https://github.com/intel/libva.git">https://github.com/intel/libva.git</a>
LLVM	18.1.8	3b5b5c1ec4a3095ab096dd780e84d7ab81f3d7ff	<a href="https://github.com/llvm/llvm-project">https://github.com/llvm/llvm-project</a>
Firmware	Main	5649ca751a23ff3b4b2b2caa4d5978af3afb5c1b	<a href="https://git.kernel.org/pub/scm/linux/kernel/git/firmware/linux-firmware.git">https://git.kernel.org/pub/scm/linux/kernel/git/firmware/linux-firmware.git</a>
Vulkan	2024.Q3.1	b6b48ef02da28ab03226232d9bc61db2b241abeb	<a href="https://github.com/GPUOpen-Drivers/AMDVLK/tree/v-2024.Q3.1">https://github.com/GPUOpen-Drivers/AMDVLK/tree/v-2024.Q3.1</a>
<b>Supported Applications</b>			
LunarG Vulkan SDK	1.3.290.0	NA	<a href="https://vulkan.lunarg.com/sdk/home#linux">https://vulkan.lunarg.com/sdk/home#linux</a>
Vulkan CTS	1.3.9.1		<a href="https://github.com/KhronosGroup/VK-GL-CTS/tree/vulkan-cts-1.3.9.1">https://github.com/KhronosGroup/VK-GL-CTS/tree/vulkan-cts-1.3.9.1</a>
RGP	2.2	NA	<a href="https://github.com/GPUOpen-Tools/radeon_gpu_profiler/tree/v2.2">https://github.com/GPUOpen-Tools/radeon_gpu_profiler/tree/v2.2</a>

## Chapter 5 Features Supported on Ryzen™ Embedded processors

Supported features are shown in the following table.

Feature Group	Feature Supported	V1500	V3000
<b>2D</b>	2D acceleration	NA	NA
<b>3D</b>	EGL 1.4, 1.5, EGL extensions.	NA	NA
	OGL 4.5, OGL 4.6	NA	NA
	GLX 1.4	NA	NA
	DRI3 support	NA	NA
	DRI3 updates (VDPAU, VAAPI)	NA	NA
	Vulkan Open Source	NA	NA
<b>2D</b>	10 bit Display	NA	NA
<b>Audio</b>	DP Audio supports for standard	NA	NA
<b>Audio</b>	I2S Audio	NA	NA
<b>Display</b>	EDID (Basic)	NA	NA
<b>Display support</b>	X and Desktop support	NA	NA
	Tear Free Desktop	NA	NA
	Partial support RandR 1.4 capabilities	NA	NA
	Kernel Mode Setting	NA	NA
	4K60Hz display support	NA	NA
	Multi-GPU support (see table below for dGPU pairing)	NA	NA
	Number of displays supported (see display support table below)	NA	NA
	4K cinema	NA	NA
	DP MST	NA	NA
	Single Large Surface (SLS)	NA	NA
<b>Play back</b>	Play back support MPV player using VAAPI / VDPAU	NA	NA
	Play back support for Gstreamer using VAAPI, gstomx (not recommended)	NA	NA
	1080p 24fps, 30 fps and 60fps video play back	NA	NA
	4k 30fps video play back	NA	NA
	4k 60fps video play back	NA	NA
<b>Power Management</b>	Power Play support to re-clock	NA	NA

Feature Group	Feature Supported	V1500	V3000
	Initial GPU reset support	NA	NA
	Power Play sysfs interface for manually selecting clock speeds	NA	NA
	S3	Yes	Yes
	S5	Yes	Yes
<b>VDDPAU Post Processing</b>	Deinterlace	NA	NA
<b>VDDPAU Post Processing</b>	Edge Enhancement	NA	NA
<b>VAAPI Postprocessing</b>	Deinterlace	NA	NA
<b>Transcode</b>	4k Encode	NA	NA
<b>Video Quality</b>	Scaling and color space conversion (CSC)	NA	NA
	Pull down detection and Deinterlacing	NA	NA
	Support for software scaling	NA	NA
	Support for hardware scaling	NA	NA
	10-bit Decode with 10-bit render	NA	NA
<b>Compute</b>	OpenCL	NA	NA
<b>dGMA –OpenGL</b>		NA	NA
<b>dGMA - OpenCL</b>		NA	NA
<b>fTPM</b>		Yes*	Yes
<b>RJ45-10G-Base-T (Marvell PHY)</b>	10M	Yes*	Yes
	100M	Yes*	Yes
	1G	Yes*	Yes
	2.5G	No	Yes
	10G	Yes*	Yes
<b>SFP+ (connector)</b>	10M	No	Yes
	100M	No	Yes
	1G	Yes*	Yes
	10GBASE_KR [AN=OFF, ON]	NA	Yes
	2.5G [AN=OFF]	NA	Yes
	10G	Yes*	Yes
<b>AIC1 – Inphi CS4223 Optical Fiber PHY (SFP+)</b>	10M/100M/1G/10G	NA	Yes
<b>AIC1 – TI DS125 Series Re-Timer (SFP+)</b>	10M/100M/1G/10G	NA	Yes
<b>AIC2 – 1G-Base-T (Marvell 88E1512P)</b>	10M/100M /1G	NA	Yes
<b>AIC2 – 10G-Base-T (Marvell AQR113C)</b>	10M/100M/1G/2.5G/10G	NA	Yes
<b>AIC3 – 1G-Base-T</b>	10M/100M /1G	NA	Yes

Feature Group	Feature Supported	V1500	V3000
<b>(Broadcom BCM54220)</b>			
<b>AIC3 – 10G-Base-T (Broadcom BCM84892)</b>	100M/1G/2.5G/10G	NA	Yes
<b>eMMC</b>	BC	Yes	No
	HS200	Yes	No
	HS400	Yes	No
	(USB/PCIe to eMMC bridge)	No	Yes
<b>SD Card</b>	SD UHS I – SDR50	Yes	No
	SD UHS I – SDR104	Yes	No
	SD UHS I – SDR104	Yes	No
<b>Peripherals (I/O)</b>	I2C	Yes	Yes
	USB	Yes	Yes
	USB 4.0	No	Yes
	SATA	Yes	Yes
	UART	Yes	Yes
	WDT	Yes	Yes
	SMBUS	Yes	Yes
	SPI Kernel Driver	Yes**	Yes**
	eSPI	No	Yes

\*Bilby platform only

\*\*To use the SPI kernel driver on Bilby/Fox platforms, the BIOS which has enabled SPI Entry in the ACPI table is required. The default BIOS does not have this feature. Please contact your FAE for the required BIOS.



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## Chapter 6      Features Supported on Ryzen™ Embedded processors with iGPU

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The 2024.30 Linux driver is **not supported** by any AMD Embedded dGPUs.

HW codec and display support is only applicable to Ryzen Embedded processors with integrated graphics.

### 6.1      Display Support:

Platform	Max Number of external 4K display(s)
V1500	NA
V3000	NA

## Chapter 7 Platforms Supported

Embedded SoC Version	Models/OPN's	AMD Customer Reference board
Ryzen Embedded <b>V3000</b> Series	V3C48, V3C44, V3C18I, V3C18, V3C16, V3C14, V3G18i, V3G48	FOX
Ryzen Embedded <b>V1500</b>	YE1500C4T4MFH, YE1500C4T4MFB	BILBY

## Chapter 8 Tested Platform Configurations

The following tables show the system configuration that was used for testing the driver package.

<b>V3000 Series</b>	
CPU	V3000
OPNs	Latest Revision: AIC1; AIC2 ; AIC3; B1-DVT Sampels: V3C48;V3C18i;V3C44; V3C14; V3C16;V3C18, V3G18i, V3G48;
Board Type	Fox, Direct Mount
TDP	V3C48 (8-core 45W CPU) V3C44 (4-core 45W CPU) V3C18I (8-core 15W CPU extended temperature) V3C18 (8-core 15W CPU) V3C16 (6-core 15W CPU) V3C14 (4-core 15W CPU) V3G18I (8-core 15W CPU) V3G48 (8-core 45W CPU)
BIOS version	RFX100BA (Insyde) RFE1007A (EDKII)
Memory (DDR5)	2x16 GB [Direct Mount]
DIMMs	DDR5, 4800 MT/s
Storage disk	Samsung M.2 NVME 500 Gb and SATA SSD Crucial 250 Gb
Ethernet connectors	<ul style="list-style-type: none"> <li>• 10G SFI Optical: Finisar (FTLX8574D3BCV and FTLX8574D3BCL), Intel (FTLX8574D3BCV-IT)</li> <li>• 10G Optical DAC: Fiberstore (SFPP-A020)</li> <li>• 10G KR (Backplane): Molex DAC cable (747521101) and AMPHENOL SFP DAC CABLE (571540002)</li> <li>• 1G Bel SFP [SFP-1GBT-06] and Finisar [FCLF8520P2BTL]</li> </ul>

<b>V1500</b>	
CPU	V1500
OPNs	YE1500C4T4MFH, YE1500C4T4MFB
TDP	16-25W
BIOS version	RBB120DA_RV_PCO
RAM	16GB (2x8GB DDR4 2400)
Storage disk	M.2 SATA

## Chapter 9 Issues Fixed

### V3000 Issues:

1. Fixed: Low Data rate speeds are observed at 33Mhz and 66 Mhz with SPI NAND.

## Chapter 10 Known Issues/Limitations

### Generic Issues

1. SW installation time increased due to transition from Debian(install.sh) installation to source build(rbh.sh) installation.

Steps	Time Taken				
	R2000	V2000	V1000/R1000	V1500P	V3000
sudo ./rbh.sh --prep kernel_source 2>&1   tee rbh_prepkernel.log	4m 51s	2m 54s	4m 12s	3m	2m 32s
sudo ./rbh.sh --build kernel_source 2>&1   tee rbh_buildkernel.log	55m 35s	23m 11s	47m 52s	1hr 16m 2s	29m 47s
sudo ./rbh.sh --builddall 2>&1   tee rbh_builddall.log	55m 33s	32m 26s	49m 9s	1hr 20m 40s	32m 22s
sudo ./rbh.sh --postinstall 2>&1   tee rbh_postinstall.log	8s	7s	8s	9s	8s
<b>Total Time taken</b>	1hr 56m 7s	58m 38s	1hr 41m 13s	2hr 39m 51s	1hr 4m 49s

### V3000 Issues:

1. Use Ethernet DAC cable of length  $\leq 5$  meters.
2. V3000 is cpu variant, so make sure to add “nomodeset” in grub param
3. V3000 need to use in headless mode. If display is needed, then use E9175 dGPU
4. UART provisioned for 1 x4 wire and 4 x2 wire modes only
5. On Fox RJ45 and 2.5G or 1G speed selection in BIOS, hot-plug and hot-insert of cable always triggering speed switching to 10G. Issue specific to V3C18i OPN.
6. Ethernet stability issues
  - a. AIC1 InPhi Phy 10G link stability issue in P2P mode only; mitigating with switch as link partner instead of another Fox
  - b. Link detection issues on SFP Port 0/1 for 1G speed with FS copper module (SFP-GB-GE-T 1000BASE-T) with Cat 5 UTP cable

- c. Link up failure issue after S3 on SFP+ Connector, with 1G/100M/10M as speed and 1G Bel modules connected on both the ports
  - d. Link up issues after S3 on AIC2-1G-BaseT phy ports
  - e. AIC3 BCM 10G phy has link stability issues for 100M/2.5G/1G speed modes, when using “ifconfig <i/f> down” command
    - i. For any BCM related phy issues, get in touch with BCM support
  - f. AIC3 Link up success but fails to get DHCP IP for 100M speed with 10G PHY
7. Refer “FOX Platform User Guide (ID: 57102)” from <https://devhub.amd.com/reference-platform/fox/> for USB-C J60 port, RJ45, AIC1, AIC2 and AIC3 rework details

**XGBE:**

1. [XGBE]: Force mode(Auto negotiation disabled) is not supported in RJ45.
2. Can't concurrently enable SFP+ and RJ45 interfaces.
3. No receive Split header support.

Below is the type of SFP/RJ45 modules used in the XGBE validation of this release.

Type	Model	Part Number
1G SFP - RJ45	BEL	SFP-1GBT-06
10G SFP+ DAC 1m	Finisar	SFPP-PC01
10G SFP optical	Intel	AFBR-709DMZ-IN2

# Chapter 11 Support

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Please contact your Field Applications Engineer for support on this release.